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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/825,614	04/15/2004	James P. Moffatt	6-2	7877
7590 03/08/2006			EXAMINER CHOE, HENRY	
Ryan, Mason & Lewis, LLP Suite 205 1300 Post Road Fairfield, CT 06824			ART UNIT 2817	PAPER NUMBER

DATE MAILED: 03/08/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

H.A

<b>Office Action Summary</b>	Application No. 10/825,614	Applicant(s) MOFFATT ET AL.	
	Examiner Henry K. Choe	Art Unit 2817	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) ☒ Responsive to communication(s) filed on 14 December 2005.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) ☒ Claim(s) 1-25 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1, 10, 16, 23 and 25 is/are rejected.
- 7) ☒ Claim(s) 2-9, 11-15, 17-22 and 24 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

## DETAILED ACTION

### ***Claim Rejections - 35 USC § 102***

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1, 10, 16, 23 and 25 are rejected under 35 U.S.C. 102(e) as being anticipated by Rydin (Fig. 3).

Regarding claims 1 and 16, Rydin (Fig. 3) discloses an amplifier circuit comprising means (22) for predistorting the input signal  $[x(t)]$  based on one or more static coefficients [(COEFFICIENTS); see column 4, lines 56-58] representative of a non-linear distortion characteristic of an amplifier (26), and means (80, 34, 84) for processing the input signal  $[x(t)]$  based on a non-linear gain parameter (data in 80 and 34) that reduces an error metric (output of 32) between the input signal  $[x(t)]$  and a feedback signal (output of 26 to 30 to 32) following the amplifier (26).

Regarding claim 10, Rydin (Fig. 3) further comprising the method step of applying the predistorted signal (output of 22) to the amplifier (26).

Regarding claim 23, the static coefficients (COEFFICIENTS) in Fig. 3 of Rydin are inherently implemented in the look up table since the circuitry would not read the data without the look up table.

Regarding claim 25, Rydin (Fig. 3) discloses an amplifier circuit comprising a predistorter (22) which processes an input signal  $[x(t)]$  based on one or more static coefficients [(COEFFICIENTS); see column 4, lines 56-58] representative of a non-

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linear distortion characteristic of an amplifier (26) and a non-linear gain parameter (data in 80 and 34) that reduces an error metric (output of 32) between the input signal  $[x(t)]$  and a feedback signal (output of 26 to 30 to 32) following the amplifier (26), and an adaptive circuit (80, 34, 84) which adjusts the non-linear gain parameter (data in 80 and 34) based on the error metric (output of 32).

***Allowable Subject Matter***

Claims 2-9, 11-15, 17-22 and 24 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

***Conclusion***

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Patent number (6,275,685) is the amplifier circuit with the predistorter.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Henry Choe whose telephone number is (571) 272-

1760.



**HENRY CHOE  
PRIMARY EXAMINER**